

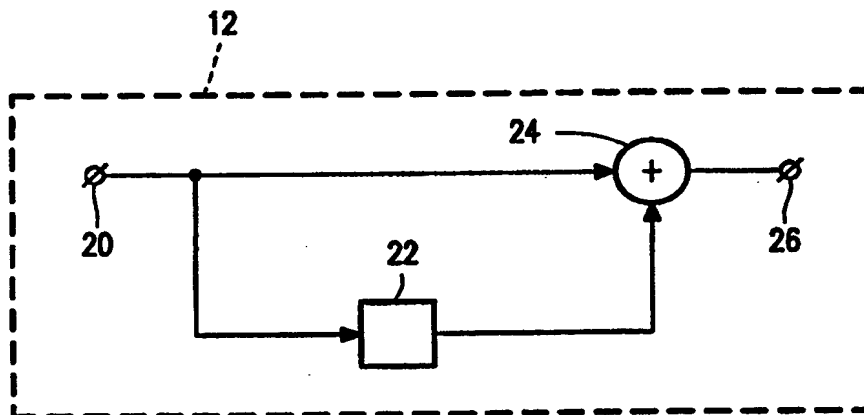


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(21) International Application Number: PCT/IB98/01648 (22) International Filing Date: 19 October 1998 (19.10.98) (30) Priority Data: 97203440.9 7 November 1997 (07.11.97) EP (71) Applicant: KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL). (71) Applicant (for SE only): PHILIPS AB [SE/SE]; Kottbygatan 7, Kista, S-164 85 Stockholm (SE). (72) Inventor: AARTS, Ronaldus, Maria; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). (74) Agent: GROENENDAAL, Antonius, W., M.; Internationaal Octrooibureau B.V., P.O. Box 220, NL-5600 AE Eindhoven (NL).		(81) Designated States: CN, JP, KR, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE). Published <i>With international search report. Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.</i>

(54) Title: AUDIO SYSTEM COMPRISING AUDIO SIGNAL PROCESSING CIRCUIT**(57) Abstract**

The audio system comprises a circuit (12) for processing an audio signal, which circuit (12) comprises an input (20) for receiving the audio signal and an output (26) for supplying an output signal. The circuit (12) further includes a harmonics generator (22) coupled to the input (20) for generating harmonics of the audio signal and adding means (24) coupled to the input (20) as well as to the harmonics generator (22) for supplying a sum of the audio signal and the generated harmonics to the output (26). The harmonics generator (22) comprises an integrator (34) for integrating the audio signal and, coupled thereto, resetting means (36) for resetting the integrator (34) at resetting times.



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AUDIO SYSTEM COMPRISING AUDIO SIGNAL PROCESSING CIRCUIT

The invention relates to an audio system comprising a circuit for processing an audio signal, whereby the circuit comprises an input for receiving the audio signal and an output for supplying an output signal, a harmonics generator coupled to the input for generating harmonics of the audio signal, adding means coupled to the input as well as to the harmonics generator for supplying a sum of the audio signal and the generated harmonics to the output.

The invention further relates to a circuit for processing an audio signal, a harmonics generator and a method for processing an audio signal.

An audio system according to the preamble is known from EP-A 546 619. Since the invention of the electrodynamic loudspeaker, there has been a need for greater acoustical output, especially at low frequencies. Often however, for instance in television sets or portable audio sets, this acoustical output is severely limited by the small size of the loudspeakers. It is known that this dilemma can be solved by using a psychoacoustic phenomenon often referred to as virtual pitch or missing fundamental, which evokes the illusion of a higher bass-response, while the loudspeaker does not radiate more power at those low frequencies. This illusion can be created by replacing low-frequency tones, which are present in the audio signal but cannot be reproduced by a small loudspeaker, by harmonics of these tones. The harmonics now represent the low-frequency tones.

In the known audio system a low-frequency band of an audio signal is selected and supplied to a harmonics generator for generating harmonics of the selected signal. The generated harmonics are thereafter added to the audio signal. In this way the low-frequency perception of the audio signal is improved. In the known audio system a full-wave rectifier is used as the harmonics generator, which generates only even harmonics. A drawback of the full-wave rectifier is that the amplitude of the generated harmonics decreases rapidly with the number of the harmonic, e.g. with respect to the second harmonic, the amplitude of the fourth, sixth and eighth harmonic is respectively 14 dB, 21 dB and 26 dB lower. Because of this reduction in amplitude of the generated harmonics, the virtual pitch effect cannot be fully exploited in the known audio system.

An object of the invention is to provide an audio system, wherein the harmonics generator is capable of generating harmonics, the amplitudes of which are substantially equal to each other. This object is achieved in the audio system according to the invention, which is characterized in that the harmonics generator comprises an integrator for
5 integrating the audio signal and, coupled thereto, resetting means for resetting the integrator at resetting times.

By integrating the audio signal and resetting the integrated signal at resetting times, a non-symmetrical waveform is obtained which comprises both odd and even harmonics, whereby the amplitude of the generated harmonics decreases relatively slowly with
10 the number of the harmonic. Consequently, in the audio system according to the invention, there is a relatively strong virtual pitch effect. Furthermore, because the amplitude of the generated harmonics is proportional to the amplitude of the audio signal, no annoying distortions in the output signal are introduced by the harmonics generator.

An embodiment of the audio system according to the invention is
15 characterized in that the resetting means are embodied so as to periodically reset the integrator according to a reset period. By virtue of this measure, the generation of harmonics is repeated periodically, thus providing a constant stream of harmonics in the output signal.

A further embodiment of the audio system according to the invention is characterized in that the resetting means are embodied so as to determine the reset period in
20 dependence on the period of the audio signal. This is a simple embodiment of the audio system according to the invention.

A further embodiment of the audio system according to the invention is characterized in that the resetting means are embodied so as to reset the integrator during at least a part of the reset period. By virtue of this measure, it is possible to prevent certain parts of the
25 audio signal, for example those parts where the amplitude of the audio signal is negative, from being integrated.

A further embodiment of the audio system according to the invention is characterized in that the resetting means are embodied so as to reset the integrator when the audio signal crosses a threshold value. By virtue thereof, integration of those parts of the audio
30 signal which exceed a certain threshold value can be prevented.

A further embodiment of the audio system according to the invention is characterized in that the harmonics generator further comprises a rectifier for rectifying the audio

signal, whereby the rectifier is coupled to the integrator so that the rectified signal is integrated by the integrator. By virtue of this measure, also the negative parts of the audio signal contribute to the amplitude of the generated harmonics.

Some low-frequency tones, which are reproduced by the audio system according to the invention, are perceived by human beings as having a higher loudness than the loudness of the corresponding low-frequency tones which are present in the audio signal. In order to compensate for this undesired artefact, a further embodiment of the audio system according to the invention is characterized in that the integrator is embodied so as to limit the amplitude of the integrated signal. In this way, the perceived loudness of low-frequency tones can be controlled, preferably in such a manner that the perceived loudness is substantially equal to the original loudness.

A further embodiment of the audio system according to the invention is characterized in that the integrator is embodied so as to stop the integration in dependence on the amplitude of the integrated signal. This is a simple and effective embodiment for limiting the amplitude of the integrated signal and thus the perceived loudness of low-frequency tones.

A further embodiment of the audio system according to the invention is characterized in that the integrator is embodied so as to adapt an integration time-constant in dependence on the amplitude or the frequency of the integrated signal. By virtue of this measure, the amplitude of the integrated signal can be limited gradually, enabling a smooth control of the perceived loudness of low-frequency tones.

The above object and features of the present invention will be more apparent from the following description of the preferred embodiments with reference to the drawings, wherein:

Figure 1 shows a block diagram of an audio system according to the invention,

Figure 2 shows a block diagram of a circuit for processing an audio signal according to the invention,

Figure 3 shows a block diagram of a harmonics generator according to the invention,

Figure 4 shows a first embodiment of an integrator which can be used in the present invention,

Figure 5 shows a circuit for use in the present invention, in which an integrator and a resetting means are combined,

Figures 6 and 7 show second and third embodiments, respectively, of an integrator for use in the present invention,

5 Figures 8 and 9 show first and second embodiments, respectively, of a limiter which can be used in the present invention in combination with an integrator as shown, for example, in Figures 4 and 5,

Figure 10 shows diagrams of various wave forms a..g generated in response to a sinusoidal input signal applied to a harmonics generator according to the invention.

10 In the Figures, identical parts are provided with the same reference numbers.

Figure 1 shows a block diagram of an audio system according to the invention. The audio system comprises a signal source 10, which is coupled via a circuit 12 and an amplifier 14, respectively, to a loudspeaker 16. The signal source 10 may derive its signal from a CD, a cassette or a received signal or any other audio source. The circuit 12 processes the
15 audio signal supplied by the signal source 10 in such a way that low-frequency tones, which are present in the audio signal but cannot be reproduced by the loudspeaker 16 because of its limited size, are replaced by harmonics of these tones. These harmonics, which can be reproduced by the loudspeaker 16, evoke the illusion of a higher bass response. This psychoacoustical phenomenon is often referred to as virtual pitch or missing fundamental. The audio signal, which is processed
20 by the circuit 12, is thereafter amplified by the amplifier 14. This amplified signal is then reproduced by the loudspeaker 16.

Figure 2 shows a block diagram of a circuit 12 for processing an audio signal according to the invention. The circuit 12 comprises an input 20 for receiving an audio signal and an output 26 for supplying an output signal. The circuit 12 further comprises a
25 harmonics generator 22 coupled to the input 20 and adding means 24, coupled to the input 20 and the harmonics generator 22, for supplying the sum of the audio signal and the output signal of the harmonics generator 22 to the output 26.

In the circuit 12 for processing an audio signal a first filter may be inserted between the input 20 and the harmonics generator 22. Preferably this first filter is embodied so as
30 to pass those low-frequency components in the audio signal which cannot be reproduced by the loudspeaker 16, while at the same time spurious dc components in the audio signal are blocked. It is also possible to insert a second filter in the circuit 12 between the harmonics generator 22

and the adding means 24. By means of this second filter the number of harmonics which are reproduced by the loudspeaker 16 can be controlled. Furthermore, a third filter can be inserted in the circuit 12 between the input 20 and the adding means 24. Preferably, this third filter is used to block those low-frequency components in the audio signal which cannot be reproduced by the
5 loudspeaker, thus preventing an overload of the loudspeaker 16.

Figure 3 shows a block diagram of a harmonics generator 22 according to the invention. The harmonics generator 22 comprises an input 30 for receiving an audio signal and an output 38 for supplying an output signal. The harmonics generator 22 further comprises an integrator 34 and, coupled thereto, a resetting means 36. The integrator 34 integrates the
10 audio signal received by the input 30 and supplies the integrated signal to the output 38. The resetting means 36 are embodied so as to reset the integrator 34 at resetting times. In this way, the output signal comprises both odd and even harmonics, whereby the amplitudes of these harmonics are substantially equal to each other. Furthermore, because the amplitude of the generated harmonics is proportional to the amplitude of the audio signal, no annoying distortions
15 are introduced by the harmonics generator 22.

The resetting times can be determined by the resetting means 36 in a number of different ways. The resetting means 36 can determine the resetting times in dependence on some properties of the audio signal, for instance the period, the amplitude or the zero crossings. It is also possible that the resetting means 36 determine the resetting times in dependence on
20 similar properties of the output signal. Furthermore, the resetting means 36 may determine the resetting times in dependence on both the audio signal and the output signal. It may be clear that in a specific embodiment of the harmonics generator 22 according to the invention, only one or both of the connections 35 and 37 are present.

The harmonics generator 22 may further comprise a rectifier 32, which
25 rectifies the audio signal received by the input 30.

Figure 4 shows a first embodiment of an integrator 34 which can be used in the present invention. The integrator 34 comprises an input 40 for receiving an input signal and an output 52 for supplying an output signal. The integrator 34 further comprises an operational amplifier 50, the positive input of which is grounded. A resistor 48, a capacitor 46 and a switch
30 44 are placed in parallel with each other and couple the negative input of the operational amplifier 50 to its output. This negative input of the operational amplifier 50 is also coupled, via a resistor 42, to the input 40. The output of the operational amplifier 50 is coupled to the output

52 of the integrator 34. The switch 44 is controlled by the reset signal RST, which is generated by the resetting means 36 in such a way that the switch 44 is closed at resetting times.

It will be clear to a person skilled in the art that the input signal received at the input 40 is integrated by this embodiment of the integrator 34, whereby the integrated signal 5 is supplied to the output 52. The integrator is reset, i.e. the capacitor 46 is discharged and the output signal is reset to zero, when the switch 44 is closed.

Figure 5 shows a circuit for use in the present invention, in which an integrator 34 and a resetting means 36 are combined. This circuit comprises an input 64 for receiving an input signal and an output 66 for supplying an output signal. The circuit further 10 comprises the elements of Figure 4 which are needed for the integration of the input signal, i.e. the resistors 42 and 48, the operational amplifier 50 and the capacitor 46. The switch 44 is implemented by means of the transistor 62. Because the base of this transistor 62 is coupled via an inverter 60 to the input 64, the transistor 62 conducts (i.e. the switch 44 is closed and the integrator is reset) when the input signal is negative. On the other hand, when the input signal is 15 positive, the transistor 62 does not conduct, i.e. the switch 44 is open.

Some low-frequency tones, which are reproduced by the audio system according to the invention, are perceived by human beings as having a higher loudness than the loudness of the corresponding low-frequency tones which are present in the audio signal. In order to compensate for this undesired artefact, the integrator 34 can be embodied so as to limit 20 the amplitude of the integrated signal. In this way, the perceived loudness of low-frequency tones can be controlled, preferably in such a manner that the perceived loudness is substantially equal to the original loudness.

Figures 8 and 9 show first and second embodiments, respectively, of a limiter which can be used to limit the range of the output signal of an integrator 34 as shown, for 25 example, in Figures 4 and 5. In Figures 8 and 9 the limiter comprises an inverting amplifier, which is comprised of an input 90, an output 102, an operational amplifier 100 and two resistors 92 and 98. The absolute value of the voltage gain of this inverting amplifier is equal to the resistance of the resistor 98 divided by the resistance of the resistor 92. In the limiter of Figure 8, two diodes 94 and 96, which are placed in parallel with the resistor 98, prevent an output signal 30 of the inverting amplifier from exceeding certain voltage limits. Because the positive input of the operational amplifier 100 is grounded, the voltage at the negative input is also zero (virtual ground). Thus, diode 94 conducts when the output signal is negative, i.e. when the input signal

which is received by the input 90 is positive. In the same way, diode 96 conducts when the output signal is positive, i.e. when the input signal is negative. In this way, when using silicon diodes, the range of the output signal is limited between approximately -0.6 and +0.6 volts.

In the limiter of Figure 9, the task of preventing the output signal of the
5 inverting amplifier from exceeding certain voltage limits is performed by two zener diodes 110 and 112. Here, the zener diode 110 conducts when the output signal is positive, and the zener diode 112 conducts when the output signal is negative. In this way, the range of the output signal is limited between approximately the inverted zener voltage of the zener diode 110 and the zener voltage of the zener diode 112.

10 The limiters as shown in Figures 8 and 9 can be coupled to the integrator 34 as shown, for example, in Figure 4. This coupling may for instance be effected in such a way that the output 52 of the integrator 34 is connected to the input 90 of the limiter, thus providing for a limitation of the output signal of the integrator 34. It is also possible to couple the output 102 of the limiter to the input 40 of the integrator 34, thus providing for a limitation of the input signal
15 of the integrator 34. Furthermore, it is possible to combine the function of the limiter with that of the integrator 34. Two examples of such a combination are shown in Figures 6 and 7. Figure 6 shows the combination of the limiter of Figure 8 with the integrator 34 as shown in Figure 4. The combination of the limiter of Figure 9 with the integrator 34 as shown in Figure 4 is depicted in Figure 7.

20 The integrator 34 as shown, for example, in Figure 4 may also be embodied so as to adapt an integration time-constant in dependence on the amplitude of the integrated signal. By virtue of this measure, the amplitude of the integrated signal can be limited gradually, thus enabling a smooth control of the perceived loudness of low-frequency tones. This adaptation of the integration time-constant can be achieved by altering the resistance of the resistor 42
25 and/or the capacitance of the capacitor 46. The effective resistance of the resistor 42 can, be changed, for instance by switching one or more resistors in series or parallel with the resistor 42. The effective capacitance of the capacitor 46 can be changed, for instance, by switching one or more capacitors in series or in parallel with the capacitor 46.

Figure 10 shows styled diagrams of various wave forms a..g generated in
30 response to a sinusoidal input signal applied to an harmonics generator 22 according to the invention. In these diagrams the input signal is indicated by a straight line and the generated wave form is indicated by means of a dashed line. The wave form a in Figure 10 can be

generated by the harmonics generator 22 according to the invention, in which the input signal is rectified before being integrated, whereby the integrator 34 is reset by the resetting means 36 at the end of each period of the input signal. The wave forms b and c can be generated by the harmonics generator 22 in a similar fashion, whereby, for wave form b, the integrator 34 is reset
5 at the end of each second period of the input signal, and for wave form c, the integrator 34 is reset at each zero crossing of the input signal. The wave form d can be generated by the harmonics generator 22, whereby the harmonics generator 22 comprises the combination of the integrator 34 and the resetting means 36 as depicted in Figure 5. In this case, the harmonics generator 22 does not comprise the rectifier 22.

10 Wave forms e, f, and g in Figure 10 can be generated by the harmonics generator 22 according to the invention in a similar fashion as described above for wave form a. Wave form e is generated by the harmonics generator 22, which is embodied so as to stop the integration in dependence on the amplitude of the integrated signal. Here, the harmonics generator 22 may comprise an integrator 34 as shown in Figures 6 and 7, or an integrator 34 as
15 depicted in Figure 4 in combination with a limiter circuit as shown, for example, in Figures 8 and 9.

The wave forms f and g illustrate the adaptation of an integration time-constant by the integrator 34. In order to generate the wave form f, the integration time-constant of the integrator 34 is adapted once during each period of the input signal, whereby this
20 adaptation depends on, for example, the amplitude or the frequency of the integrated signal. Waveform g may be generated in a similar fashion, whereby the integrator 34 is adapted twice during each period of the input signal. Of course it is also possible to arrange the integrator 34 in such a way that more than two adaptations of the integration time-constant are supported.

It will be obvious to those having ordinary skill in the art that many changes
25 may be made to the above-described invention without departing from the underlying principles thereof. For example, the signal processing performed in the entities according to the invention may also be performed by a dedicated integrated circuit or in software running on a programmable processor. Furthermore, in the integrator 34 as shown, for example, in Figure 4, the resistor 48 may be omitted. A desired limitation of the amplitude of the output signal of the harmonics generator 22
30 can also be achieved by means of a multiplication of the input or output signal with a certain multiplication factor.

CLAIMS:

1. An audio system comprising a circuit (12) for processing an audio signal, whereby the circuit (12) comprises an input (20) for receiving the audio signal and an output (26) for supplying an output signal, a harmonics generator (22) coupled to the input (20) for generating harmonics of the audio signal, adding means (24) coupled to the input (20) as well as
5 to the harmonics generator (22) for supplying a sum of the audio signal and the generated harmonics to the output (26), characterized in that the harmonics generator (22) comprises an integrator (34) for integrating the audio signal and, coupled thereto, resetting means (36) for resetting the integrator (34) at resetting times.
2. The audio system according to Claim 1, characterized in that the resetting
10 means (36) are embodied so as to periodically reset the integrator (34) according to a reset period.
3. The audio system according to Claim 2, characterized in that the resetting means (36) are embodied so as to determine the reset period in dependence on the period of the audio signal.
- 15 4. The audio system according to Claim 2 or 3, characterized in that the resetting means (36) are embodied so as to reset the integrator (34) during at least a part of the reset period.
5. The audio system according to any one of the preceding Claims, characterized in that the resetting means (36) are embodied so as to reset the integrator (34) when
20 the audio signal crosses a threshold value.
6. The audio system according to any one of the preceding Claims, characterized in that the harmonics generator (22) further comprises a rectifier (32) for rectifying the audio signal, whereby the rectifier (32) is coupled to the integrator (34) so that the rectified signal is integrated by the integrator (34).
- 25 7. The audio system according to any one of the preceding Claims, characterized in that the integrator (34) is embodied so as to limit the amplitude of the integrated signal.

8. The audio system according to Claim 7, characterized in that the integrator (34) is embodied so as to stop the integration in dependence on the amplitude of the integrated signal.

9. The audio system according to Claim 7 or 8, characterized in that the
5 integrator (34) is embodied so as to adapt an integration time-constant in dependence on the amplitude or the frequency of the integrated signal.

10. A circuit (12) for processing an audio signal, comprising an input (20) for receiving the audio signal and an output (26) for supplying an output signal, a harmonics generator (22) coupled to the input (20) for generating harmonics of the audio signal, adding
10 means (24) coupled to the input (20) as well as to the harmonics generator (22) for supplying a sum of the audio signal and the generated harmonics to the output (26), characterized in that the harmonics generator (22) comprises an integrator (34) for integrating the audio signal and, coupled thereto, resetting means (36) for resetting the integrator (34) at resetting times.

11. A harmonics generator (22), characterized in that the harmonics generator
15 (22) comprises an integrator (34) for integrating an audio signal and, coupled thereto, resetting means (36) for resetting the integrator (34) at resetting times.

12. A method for processing an audio signal, comprising the generation of harmonics of the audio signal and the supply of a sum of the audio signal and the generated harmonics, characterized in that the generation of harmonics includes the integration of the audio
20 signal and the resetting of a result of said integration at resetting times.

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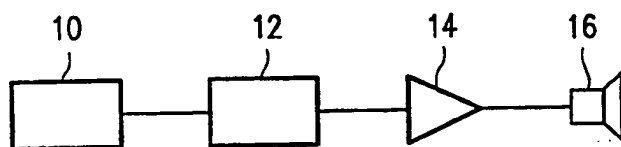


FIG. 1

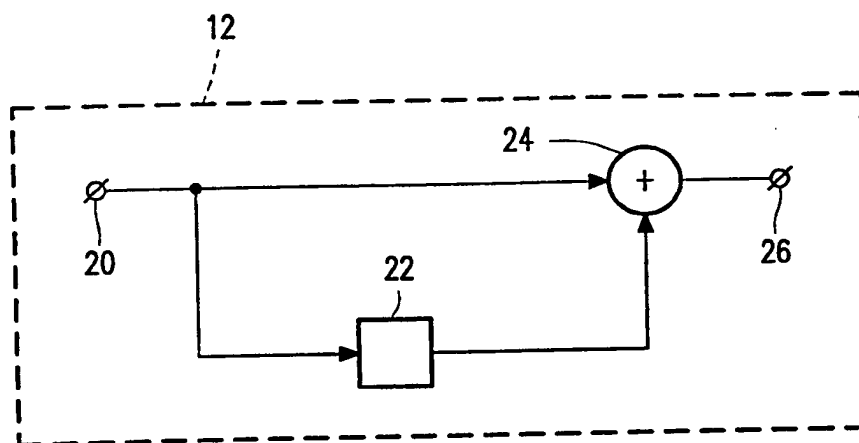


FIG. 2

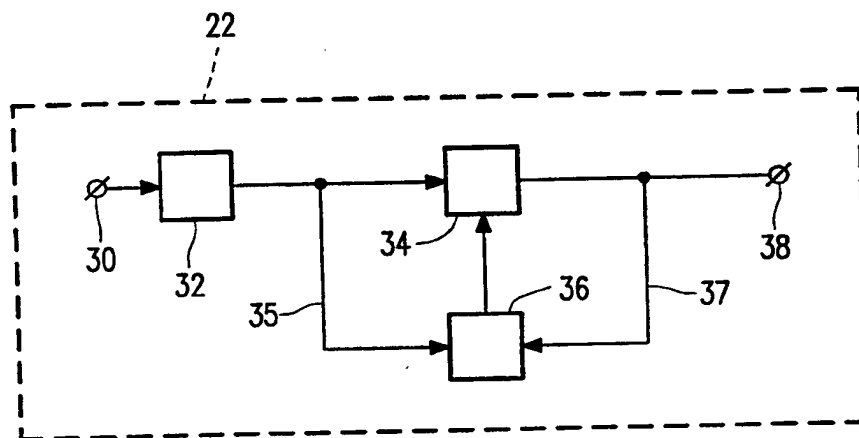


FIG. 3

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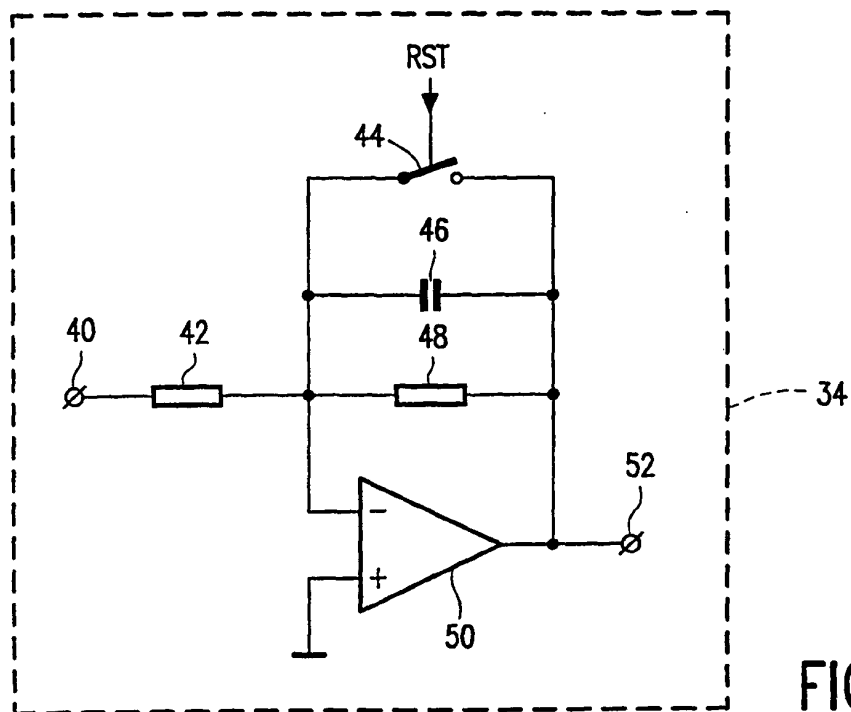


FIG. 4

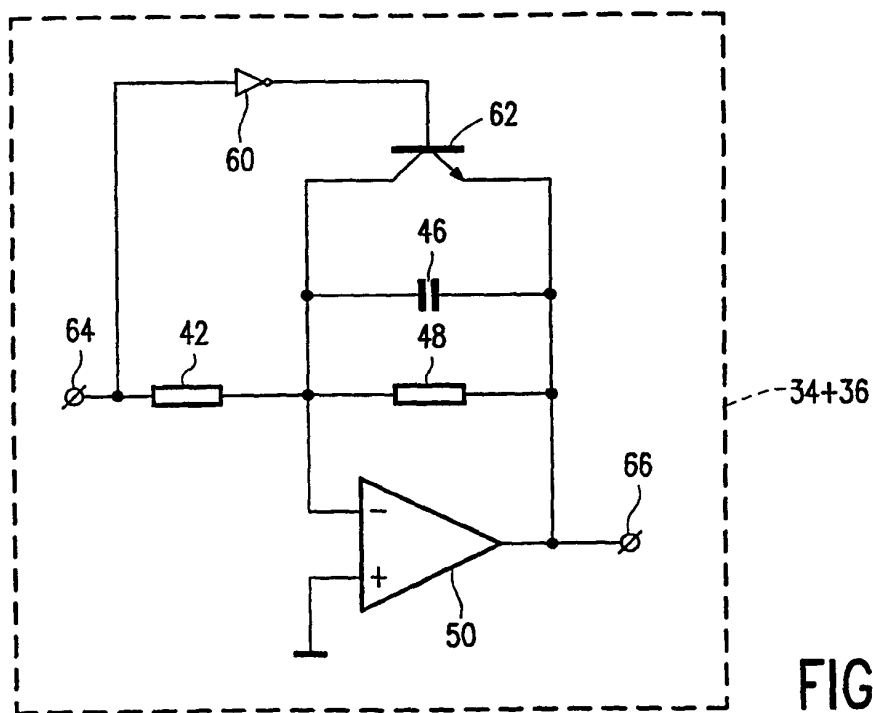


FIG. 5

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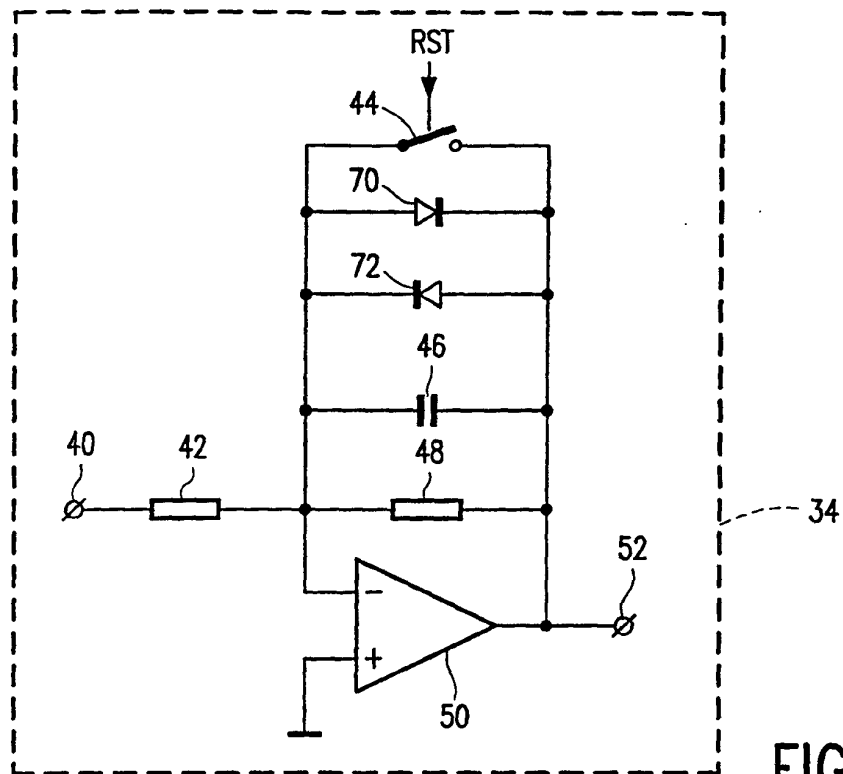


FIG. 6

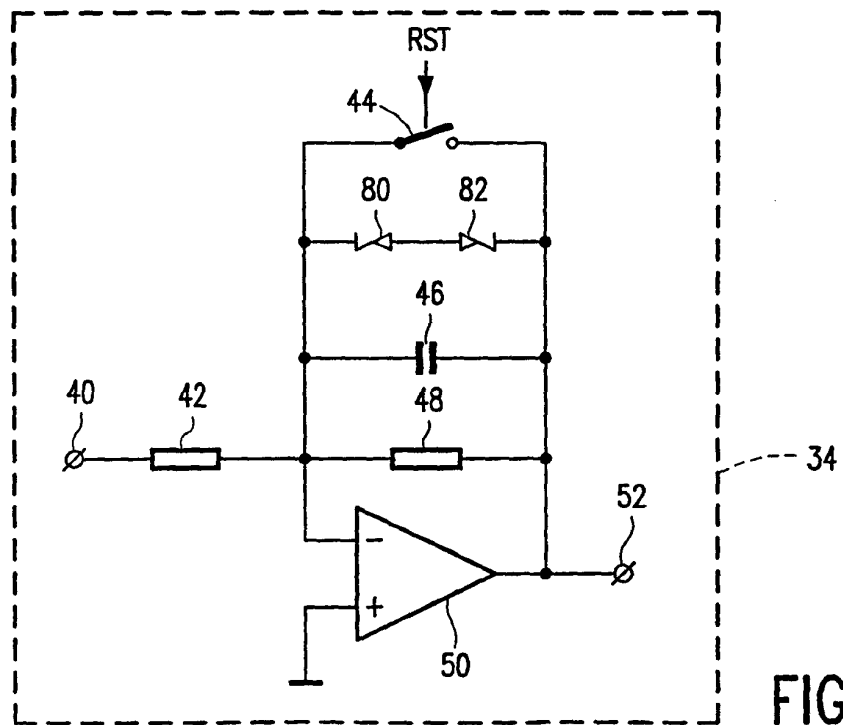


FIG. 7

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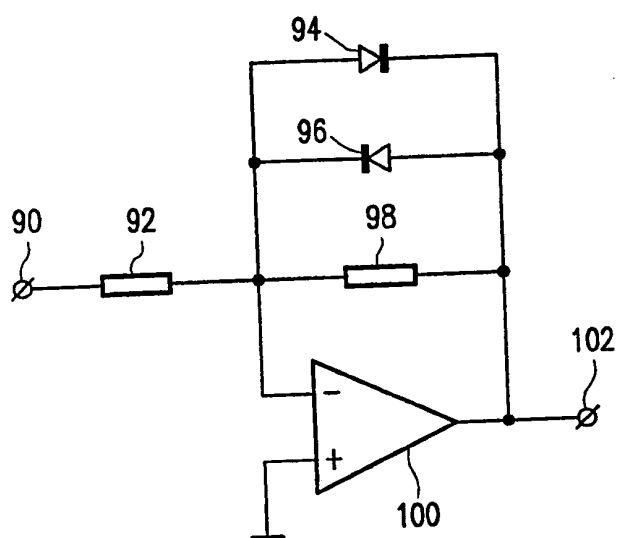


FIG. 8

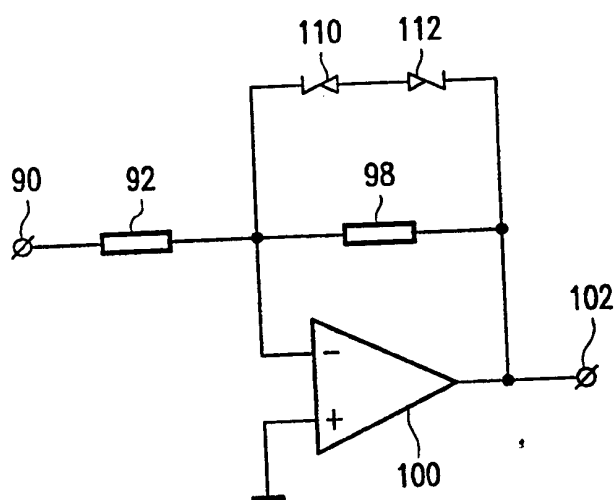


FIG. 9

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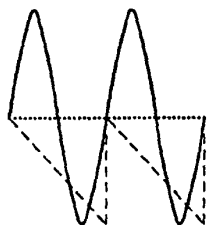
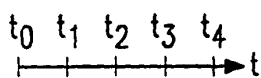


FIG. 10a

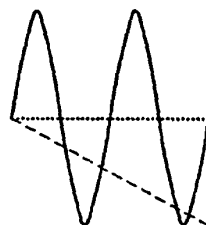
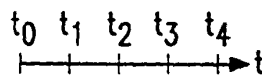


FIG. 10b

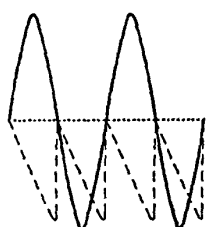


FIG. 10c

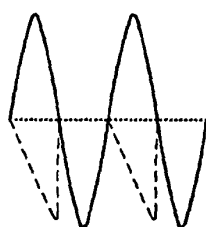


FIG. 10d

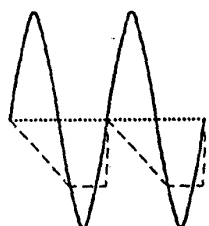


FIG. 10e

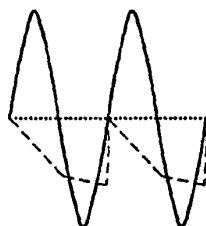


FIG. 10f

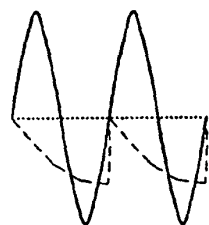


FIG. 10g

INTERNATIONAL SEARCH REPORT

International application No.

PCT/IB 98/01648

A. CLASSIFICATION OF SUBJECT MATTER

IPC6: H04R 3/04, H03G 5/18, H03G 11/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC6: H03F, H03G, H04R, H04S

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EDOC, WPIL, JAPIO

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 0647023 A2 (FEREMANS, ERIC EDMOND), 5 April 1995 (05.04.95), see the whole document --	1-12
A	EP 0546619 A2 (N.V. PHILIPS'GLOEILAMPENFABRIEKEN), 16 June 1993 (16.06.93), see the whole document --	1-12
A	Patent Abstracts of Japan, abstract of JP 7-231497 A (VICTOR CO OF JAPAN LTD), 29 August 1995 (29.08.95) -- -----	1-12

☐ Further documents are listed in the continuation of Box C.
 ☒ See patent family annex.

* Special categories of cited documents:

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Date of the actual completion of the international search

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INTERNATIONAL SEARCH REPORT
Information on patent family members

02/03/99

International application No.
PCT/IB 98/01648

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP 0647023 A2	05/04/95	BE 1007574 A	08/08/95
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		JP 5328481 A	10/12/93